

**BASE-BAND DIGITAL PRE-DISTORTION-BASED METHOD FOR IMPROVING
EFFICIENCY OF RF POWER AMPLIFIER**

Field of the Invention

5 The present invention relates to a method for improving RF power amplifier efficiency in broadband wireless communication systems, particularly to a BDPD-based (Base-band Digital Pre-Distortion) method for improving RF power amplifier efficiency.

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Background of the Invention

15 The limitation of RF bandwidth resource has stringent restrictions to out-of-band spectrum leakage or adjacent channel power leakage of special RF transmitters working in specified frequency bands. The nonlinear characteristic of RF power amplifier near the saturated working space causes the frequency spectrum of output signals under higher input signal condition higher than the frequency spectrum of the input signals, i.e., the nonlinear characteristic of RF power
20 amplifier results in out-of-band spectrum leakage. Therefore, to depress out-of-band spectrum leakage or adjacent channel power leakage, the RF power amplifier in a communication system usually works in the linear working space, which is far away from the saturated working space. However, that method severely
25 degrades efficiency of RF power amplifier and makes RF power amplifier bulky due to heat dissipation requirement, increasing the cost of RF power amplifier. To solve above problem, an effective measure is to utilize base-band digital pre-distortion technology, which requires establishing a

mathematical model for RF power amplifier to calculate pre-distortion first.

To solve sole out-of-band spectrum leakage problem, base-band digital pre-distortion technology is developed for digital mobile communication systems in recent years. The object of that technology is to avoid out-of-band spectrum leakage and low efficiency of RF power amplifiers. The main ideal of base-band digital pre-distortion technology is to carry out preprocessing (i.e., pre-distortion) for input X of RF power amplifier to obtain Xy , which ensures the output Y of RF power amplifier is linear to the original input X; when there is linear relationship between input X and output Y of RF power amplifier, boosting the power output will not cause out-of-band spectrum leakage. Since the pre-distortion is accomplished through base-band digital signal preprocessing, the RF power amplifier shown in Fig.1 actually represents the entire RF transmission channel from base-band to carrier (up-conversion) and then from carrier to base-band (down-conversion for signal feedback), including the RF power amplifier.

To solve the 'pre-distortion' algorithm in Fig.1, it is necessary to know the mathematical model of the RF power amplifier; furthermore, that mathematical model should be established specially for solving the pre-distortion algorithm, that is to say, the mathematical model requires minimum calculating workload and memory occupation to solve the pre-distortion algorithm. Such a mathematical model for RF power amplifier is usually a black-box model, i.e., it reflects (Input/Output) I/O relationship of the object (RF power amplifier). As shown in Fig.2, the difference e between output

Y of RF power amplifier and the output Y_m of the model is used to refine or correct the model. When e reaches to 0, the model represents the I/O characteristic of RF power amplifier accurately. In actual practice, when e is small enough, it is
5 deemed that the model can reflect I/O relationship of the RF power amplifier at certain accuracy.

Usually, the relationship between input X and output Y of the RF power amplifier comprises amplitude ($|X|$ and $|Y|$) and phase (Φ_{in} and Φ_{out}) of base-band digital signal X and Y ; wherein
10 the relationship between input amplitude $|X|$ and output amplitude $|Y|$ is referred as AM-AM model of RF power amplifier.

$$|Y| = f(|X|). \quad (1)$$

The relationship between input amplitude $|X|$ and I/O phase deviation $\Delta\Phi$ is referred as AM-PM model:

15
$$\Delta\Phi = g(|X|), \quad (2)$$

herein: $\Delta\Phi = \Phi_{out} - \Phi_{in}$.

However, for broadband systems, no one has studied AM-PM model successfully yet, even the opinions about the profile of typical AM-PM curve vary; most AM-PM curves in documentation
20 are sketched drawings, which are neither reliable nor supported by experimental data.

Presently, the major part of study work for model of RF power amplifier focuses on establishing AM-AM model for RF power amplifier. Wherein, a typical polynomial model (from PCT patent
25 of PMC (Canada): WO 01/05026 A1) based on I/O amplitude relationship of RF power amplifier (i.e., AM-AM model) involves some delay items of input signal. That model can be used to describe some nonlinear characteristics (e.g., saturation and

delay cycle (or, referred as memory)) of RF power amplifier in I/O amplitude aspect. However, that polynomial model has the following disadvantages:

1. The parameter estimation of the polynomial model involves
5 huge calculating workload, which increases exponentially to the order number of the polynomial.

2. After the AM-AM model of RF power amplifier is obtained with the Polynomial method, it is difficult to solve the pre-distortion algorithm corresponding to the polynomial model;
10 especially, when a higher order polynomial model has to be used due to accuracy requirement, it is nearly impossible to obtain the analytic solution of reverse function of the pre-distortion algorithm; as for numerical approach, the calculating workload increases quickly in non-linear manner as the order of the
15 polynomial increases.

3. In a self-adapting pre-distortion system, the model of RF power amplifier has to be stored in the controller; in addition, the output of the model corresponding to input of RF power amplifier has to be calculated online, in order to update
20 the model and the pre-distortion parameters of the self-adapting pre-distortion unit online. However, a polynomial model of RF power amplifier requires a huge storage space, the reason is: in the polynomial approach, as the order of the model increases, the number of the parameters of the model
25 increases; also, a group of new exponential functions will be introduced into the model (the number of the exponential functions depends on the number of delay items in the model). When the model is to be stored, the new parameters have to be stored and a group of tables is required to store those

exponential functions (or a multiplier is required), therefore the model storage space is enlarged.

4. The polynomial model (AM-AM model) has been used; however, though many efforts have been exerted in AM-PM model, there is
5 no successful implementation yet. Furthermore, the foundation of arbitrarily correlating input amplitude with I/O phase deviation solitarily in some documents is doubtful.

Summary of the Invention

10 An object of the present invention is to provide a BDPD-based method for improving efficiency of RF power amplifier, which can depress out-of-band spectrum leakage of RF power amplifier and improve efficiency of RF power amplifier effectively.

To attain said object, the BDPD-based method for improving
15 efficiency of RF power amplifier according to the present invention comprises:

(1) Determining structural parameters of a neural network as required and establishing the neural network, inputting modeling data and initial values of network parameters required
20 for establishing the neural network model of the RF power amplifier;

(2) Propagating forward with the input data and network parameters, calculating the difference between output value of the neural network and the corresponding expected output value,
25 then propagating backward along the neural network with said difference to correct the network parameters;

(3) Determining whether said difference meets the specified criterion; if so, outputting the neural network model of the RF power amplifier and going to step (4), otherwise inputting

the corrected network parameters to the neural network and going to step (2);

(4) Solving the pre-distortion algorithm of the RF power amplifier with said neural network model;

5 (5) Carrying out pre-distortion processing for input signal of the RF power amplifier with said pre-distortion algorithm and then feeding them to the RF power amplifier.

Said structural parameters comprise: the number n of delay items of input signal, the number r of neural elements on each
10 layer of the neural network, the number m of layers of the neural network; said modeling data comprises: output signal $Y(KT)$, input signal, and delay items of input signal of the power amplifier; said network parameters comprise: weight W_{ijk} and bias b_{ij} ; said output signal $Y(KT)$ of the RF power amplifier
15 is the expected output value corresponding to the input signal, i.e., the actual output value of the RF power amplifier corresponding to the input signal.

Said input signal and said delay items of the input signal are base-band digital signal amplitude $X(KT)$ of the power
20 amplifier and delay items thereof $X[(K-1)T] \dots X[(K-n+1)T]$, respectively.

Herein, the number n of delay items of input signal is: $1 < n < 10$, the number r of neural elements on each layer of the neural network is: $1 < r < 10$, the number m of layers of the neural
25 network is: $1 < m < 10$.

Said input signal and said delay items of input signal are base-band digital signal amplitude $X(KT)$ of the power amplifier and delay items thereof $X[(K-1)T], X[(K-2)T], \dots, X[(K-n+1)T]$

as well as phase $\Phi_{in}(KT)$ of the base-band digital signal and delay items thereof $\Phi_{in}[(K-1)T]$, $\Phi_{in}[(K-2)T]$, ..., $\Phi_{in}[(K-n+1)T]$; the number of delay items of the input signal comprises the number $n1$ of delay items of base-band digital signal amplitude and the number $n2$ of delay items of base-band digital signal phase.

Herein, $1 < n1 < 5$, $1 < n2 < 10$, $1 < r < 10$, $1 < m < 10$.

Said step (2) comprises:

(71) Calculating the corresponding intermediate variables V_{ij} of the neural network with network parameters W_{ijk} of each layer of the neural network;

(72) Activating the function to calculate the output value Y_{ij} of each neural element in the corresponding neural network through the intermediate variables V_{ij} and the neural elements;

(73) Magnifying the output value of the neural elements on the last layer of the neural network for m times to obtain the output value $Y_m(KT)$ of the neural network, herein the value of M being higher than the saturation level of the power amplifier;

(74) Calculating the difference between $Y_m(KT)$ and actual output $Y(KT)$ of the power amplifier;

(75) Magnifying the difference $e(kT)$ between $Y_m(KT)$ and $Y(KT)$ for $-m$ times and calculating $\Omega(V_{ij})$ with output value V_{ij} of the neural elements on the last layer of the network, herein, $\Omega(v) = d\Psi(v)/dv$;

(76) Multiplying $Me(kT)$ with $\Omega(V_{ij})$ to obtain δ_{ij} ;

(77) Propagating δ_{ij} backward along the network channel, in which propagating forward is carried out, with current values

of network parameters and obtaining the intermediate variables $u_{i1}, u_{i2}, \dots, u_{ir}$;

(78) Calculating intermediate variables $\delta_{i1}, \delta_{i2}, \dots, \delta_{ir}$ with $u_{i1}, u_{i2}, \dots, u_{ir}$ and current network parameters;

5 Herein, $\delta_{i1}, \delta_{i2}, \dots, \delta_{ir}$ are obtained through multiplying $\Omega(V_{i1}), \Omega(V_{i2}), \dots, \Omega(V_{ir})$ with $u_{i1}, u_{i2}, \dots, u_{ir}$ respectively, said $\Omega(V_{i1}), \Omega(V_{i2}), \dots, \Omega(V_{ir})$ are calculated out with intermediate variable $v_{i1}, v_{i2}, \dots, v_{ir}$;

(79) Updating current network parameters with $\delta_{i1}, \delta_{i2}, \dots, \delta_{ir}$, and calculating c with the following equation: $c = [\sum (\delta_{i1}^2 + \delta_{i2}^2 + \dots + \delta_{ir}^2) + \delta_{ij}^2]^{1/2}$;

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Wherein when updating the current network parameters, the updated network parameters W_{ijk} and b_{ij} are calculated out as follows:

15 $W_{ijk} =$ value of network parameter before update $- \eta \times \delta_{ij} \times$ output value of corresponding neural elements, herein η is the searching step length;

$b_{ij} =$ value of network parameter before update $- \eta \times \delta_{ij}$;

Said step (3) comprises: determining whether c meets the criterion; if so, outputting the neural network model of the RF power amplifier, otherwise inputting the corrected network parameters W_{ijk} and b_{ij} to the neural network and going to step (71).

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Said $K = 2 \times$ mean gain k_b of RF power amplifier.

25 The bandwidth of said input signal is wider than that of actual input signal of RF power amplifier.

It can be seen from above technical solution that the present invention provides a new BDPD-oriented modeling method for RF power amplifier, i.e., a neural network method, which comprises AM-AM neural network method orienting to I/O amplitude relationship of RF power amplifier, neural network method orienting to phase bias model of RF power amplifier, and structural design and parameter estimation for the neural network model in the method. According to the neural network for establishing mathematical model for RF power amplifier, the calculating workload increases in linear manner as the model becomes more complex; and said neural network model is particularly suitable for solving pre-distortion algorithm.

In addition, since the only nonlinear function of the model is an activation function, only one table is required to store the nonlinear function when the corresponding self-adapting digital pre-distortion system is implemented, therefore, the model requires small storage space when storage is performed, and the required storage space increases little as the model becomes more complex, because only the new network parameters are to be stored and the category and number of nonlinear function keeps constant; the phase bias model according to the present invention correlates I/O phase bias and input amplitude, input phase, and their delay items, so that a more reliable and feasible phase bias model for RF power amplifier is established, which takes account all external factors that may affect phase bias into input variables of the model; therefore, the model is a RF power amplifier I/O phase bias model that is more sophisticated than traditional AM-PM models.

Therefore, the present invention attains the object of

improving efficiency of RF power amplifier through establishing an accurate mathematical model for RF power amplifier.

Brief Description of the Drawings

5 Fig.1 is a schematic diagram of improving efficiency of RF power amplifier;

 Fig.2 is a schematic diagram of establishing the RF power amplifier model;

10 Fig.3 is a schematic diagram of AM-AM model orienting to I/O amplitude relationship of RF power amplifier;

 Fig.4 is a schematic diagram of the neural network model used in establishing AM-AM model for RF power amplifier;

 Fig.5 is a calculating flowchart for establishing AM-AM model for RF power amplifier;

15 Fig.6 shows the neural network modeling method for establishing phase bias model for RF power amplifier;

 Fig.7 is a schematic diagram of AM-AM characteristic of actual RF power amplifier;

20 Fig.8 is a schematic diagram of characteristic of modeling signals;

 Fig.9 is a schematic diagram of characteristic of model verification signals;

 Fig.10 is a schematic diagram of the model verification result.

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Detailed Description of the Embodiments

The BDPD-based method for improving efficiency of RF power amplifier according to the present invention improves efficiency of RF power amplifier through carrying out

pre-distortion processing for input of RF power amplifier; the specific pre-distortion processing depends on the mathematical model of the RF power amplifier; the present invention improves efficiency of RF power amplifier through providing a method for
5 establishing an accurate mathematical model for RF power amplifier; the present invention relates to a BDPD-based modeling method, i.e., neural network method for RF power amplifier, which comprises AM-AM neural network modeling method orienting to I/O amplitude relationship of RF power amplifier
10 shown in Fig.3, AM-PM neural network modeling method orienting to phase bias of RF power amplifier shown in Fig.6, and structural design and parameter estimation for the neural network models in above method.

Hereunder the embodiments for improving efficiency of RF
15 power amplifier through establishing AM-AM model for RF power amplifier are described in further detail with reference to Fig.4 and Fig.5.

Step 1: Determine structural parameters of a neural network as required and establish the neural network; input modeling
20 data and initial values of network parameters required for establishing the neural network model of the RF power amplifier;

The structural parameters comprise: the number n of delay items of input signal, the number r of neural elements on each layer of the neural network, the number m of layers of the neural
25 network; the modeling data comprises: output signal $Y(KT)$, input signal, and delay items of input signal of the power amplifier; said output signal $Y(KT)$ of the RF power amplifier is the actual output value of the RF power amplifier corresponding to the input signal; the input signal and said

delay items of input signal are base-band digital signal amplitude $X(KT)$ of the power amplifier and delay items thereof $X[(K-1)T] \dots X[(K-n+1)T]$; said network parameters comprise weight W_{ijk} and bias b_{ij} ;

5 Herein, the structural parameters of the neural network are selected as follows: $1 < n < 10$, $1 < r < 10$, $1 < m < 10$;

Wherein the K value in base-band digital signal function is $2 \times$ the gain K_b of RF power amplifier; as shown in Fig.7, in the AM-AM characteristic curve obtained with actual measured
10 data of RF power amplifier, K_b is the rough mean gain of the linear segment;

The network parameters of the neural network shown in Fig.4 are: $r=3$, $m=2$, $n=9$; choose $K=10$; choose initial value $w_{ijk}(0)$ and $b_{ij}(0)$ for network parameters W_{ijk} and b_{ij} respectively and
15 then input them to the neural network; input $x(10T)$ to $x_m(KT)$ of the neural network; input $x(9T)$ to $x_m[(k-1)T]$; input $x(8T)$ to $x_m[(k-2)T]$, and input $x(7T)$ to $x_m[(k-3)T]$, ..., input $x(T)$ to $x_m[(k-9)T]$.

After the neural network is established and the modeling
20 data and network parameters are inputted, propagate forward with the input data network parameters to calculate output value of the neural network, and then propagate backward along above neural network with the difference between the output value and the expected output value corresponding to the input signal to
25 correct the network parameters; wherein the expected output value is the output value $Y(KT)$ of the RF power amplifier where the modeling data is inputted.

Step 2: calculate out the corresponding intermediate

variable V_{ij} in the neural network with network parameters W_{ijk} , input signal and delay items of the input signal on each layer of the neural network;

i.e., calculate out V_{11} , V_{12} and V_{13} in the neural network shown in Fig.4, herein:

$$V_{11} = W_{111} \times X_m(KT) + W_{121} \times X_m[(K-1)T] + \dots + W_{1,10,1} \times X_m[(K-9)T] + b_{11};$$

$$V_{12} = W_{112} \times X_m(KT) + W_{122} \times X_m[(K-1)T] + \dots + W_{1,10,2} \times X_m[(K-9)T] + b_{12};$$

$$V_{13} = W_{113} \times X_m(KT) + W_{123} \times X_m[(K-1)T] + \dots + W_{1,10,3} \times X_m[(K-9)T] + b_{13}.$$

Step 3: take intermediate variables V_{ij} as independent variables and utilize the neural element activation function to calculate neural element output value Y_{ij} in the neural network; take output value of neural elements on the intermediate layer of the neural network as input value of the neural elements on the lower layer and utilizes them to calculate the corresponding intermediate variable V_{ij} .

The activation function used in the calculation is:

$$\Psi(v) = (1 - e^{-Kv}) / (1 + e^{-Kv});$$

Take the calculated V_{11} , V_{12} , and V_{13} as independent variables to calculate Y_{11} , Y_{12} , and Y_{13} shown in Fig.4; then utilize the calculated Y_{11} , Y_{12} , and Y_{13} to calculate V_{21} , herein $V_{21} = W_{211} \times Y_{11} + W_{221} \times Y_{12} + W_{231} \times Y_{13} + b_{21}$; finally, take V_{21} as the independent variable and utilize the activation function to calculate Y_{21} .

Step 4: Magnify the output value Y_{21} of the neural elements on the last layer of the neural network for m times to obtain

the output value of the neural network $Y_m(KT)$; wherein the value of m is higher than the saturation level of the RF power amplifier;

Step 5: Calculate the difference $e(KT)$ between $Y_m(KT)$ and
 5 actual output $Y(KT)$ of RF power amplifier; wherein the calculating result in the first forward propagation shown in Fig.4 is $e(10T)$.

Thus a forward propagation along the neural network is finished.

10 Step 6: utilize the $e(10T)$ to propagate backward along the neural network; magnify $e(10T)$ for $-M$ times and calculate out $\Omega(V_{ij})$ with the output value of the neural elements on the last layer, herein, $\Omega(v) = d\Psi(v)/dv$;

As shown in Fig.4, $\Omega(V_{21})$ is calculated out with the
 15 calculated V_{21} ;

Multiply $-Me(kT)$ with $\Omega(V_{ij})$ to calculate δ_{ij} , i.e., in Fig.4, $\delta_{21} = \Omega(V_{21})Me(10T)$;

Propagate the variable δ_{ij} backward along the network channel, in which propagating forward is carried out, with
 20 current values of network parameters and obtain intermediate variable $u_{i1}, u_{i2}, \dots, u_{ir}$, i.e., calculate out u_1, u_2 , and u_3 in Fig.4 with current network parameters;

Then utilize $u_{i1}, u_{i2}, \dots, u_{ir}$ and current network parameters to calculate out intermediate variable $\delta_{i1}, \delta_{i2}, \dots$,
 25 δ_{ir} ;

Wherein $\delta_{i1}, \delta_{i2}, \dots, \delta_{ir}$ are calculated out through

multiplying u_{i1} , u_{i2} , ..., u_{ir} with $\Omega(V_{i1})$, $\Omega(V_{i2})$, ..., $\Omega(V_{ir})$, which is calculated out from the intermediate variables v_{i1} , v_{i2} , ..., v_{ir} in the neural network respectively;

Update current network parameters with δ_{i1} , δ_{i2} , ..., δ_{ir} , and

5 calculate out c with the following equation: $c = [\sum(\delta_{i1}^2 + \delta_{i2}^2 + \dots + \delta_{ir}^2) + \delta_{ij}^2]^{1/2}$;

Wherein, the updated network parameters W_{ijk} and b_{ij} are respectively:

W_{ijk} = value of the network parameter before update - $\eta \times \delta_{ij}$
 10 \times output value of corresponding neural elements, herein η is the searching step length;

b_{ij} = value of the network parameter before update - $\eta \times \delta_{ij}$;

As shown in Fig.4, calculate $\Omega(v_{11})$, $\Omega(v_{12})$ and $\Omega(v_{13})$ with v_{11} , v_{12} and v_{13} obtained during forward propagation; multiply
 15 $\Omega(v_{11})$, $\Omega(v_{12})$ and $\Omega(v_{13})$ with u_1 , u_2 and u_3 respectively to obtain δ_{11} , δ_{12} , and δ_{13} ; update $w_{211}(0)$, $w_{221}(0)$, $w_{231}(0)$ and $b_{21}(0)$ with above δ_{21} ; the detailed calculation is as follows:

$$w_{211}(1) = w_{211}(0) - \eta \times \delta_{21} \times y_{11}(0)$$

20 $w_{221}(1) = w_{221}(0) - \eta \times \delta_{21} \times y_{12}(0)$

$$w_{231}(1) = w_{231}(0) - \eta \times \delta_{21} \times y_{13}(0)$$

$$b_{21}(1) = b_{21}(0) - \eta \times \delta_{21}$$

Wherein η is the searching step length, which is adjusted as required.

25 Utilize the obtained δ_{11} , δ_{12} , δ_{13} to update the network

parameters $w_{111}(0)$, $w_{112}(0)$, $w_{113}(0)$, $b_{11}(0)$, $w_{121}(0)$,
 $w_{122}(0)$, $w_{123}(0)$, $b_{21}(0)$, $w_{131}(0)$, $w_{132}(0)$, $w_{133}(0)$, $b_{21}(0)$
etc., on the first layer, as follows:

$$w_{111}(1) = w_{111}(0) - \eta \times \delta_{11} \times x_m(10T)$$

$$5 \quad w_{121}(1) = w_{121}(0) - \eta \times \delta_{11} \times x_m(9T)$$

$$w_{131}(1) = w_{131}(0) - \eta \times \delta_{11} \times x_m(8T)$$

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$$w_{1,10,1}(1) = w_{1,10,1}(0) - \eta \times \delta_{11} \times x_m(T)$$

$$b_{11}(1) = b_{11}(0) - \eta \times \delta_{11}$$

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$$w_{112}(1) = w_{112}(0) - \eta \times \delta_{12} \times x_m(10T)$$

$$w_{122}(1) = w_{122}(0) - \eta \times \delta_{12} \times x_m(9T)$$

$$w_{132}(1) = w_{132}(0) - \eta \times \delta_{12} \times x_m(8T)$$

.....

$$15 \quad w_{1,10,2}(1) = w_{1,10,2}(0) - \eta \times \delta_{12} \times x_m(T)$$

$$b_{21}(1) = b_{21}(0) - \eta \times \delta_{12}$$

$$w_{113}(1) = w_{113}(0) - \eta \times \delta_{13} \times x_m(10T)$$

$$w_{123}(1) = w_{123}(0) - \eta \times \delta_{13} \times x_m(9T)$$

$$20 \quad w_{133}(1) = w_{133}(0) - \eta \times \delta_{13} \times x_m(8T)$$

.....

$$w_{1,10,3}(1) = w_{1,10,3}(0) - \eta \times \delta_{13} \times x_m(T)$$

$$b_{31}(1) = b_{31}(0) - \eta \times \delta_{13}$$

Wherein η is the searching step length, which is adjusted

as required.

In addition, calculate out c in Fig.4 with the equation:

$$c = (\delta_{11}^2 + \delta_{12}^2 + \delta_{13}^2 + \delta_{21}^2)^{1/2}.$$

Step 7: determine whether c meet the criterion; if so, go
5 to step 8; otherwise input the adjusted network parameters W_{ijk}
and b_{ij} to the neural network and set $K=K+1$, i.e., set $K=11$ in
the neural network shown in Fig.4, and then go to step 2;

If c is small enough, terminate the training process. The
satisfactory value of c may be determined according to the
10 convergence situation of the model during training process. In
the initial phase, a fixed number of iterations may be set as
the condition to terminate the training process.

Step 8: output the neural network model for RF power
amplifier and go to step 9.

15 Step 9: Solve the pre-distortion algorithm of the RF power
amplifier with the neural network model.

Step 10: Carry out pre-distortion processing for input
signal of the RF power amplifier with the pre-distortion
algorithm and then feed them to the RF power amplifier.

20 Through above process, an accurate AM-AM neural network
model orienting to I/O amplitude relationship of RF power
amplifier can be established and can be used to solve
pre-distortion algorithm for the RF power amplifier to improve
efficiency of RF power amplifier. In addition, the
25 pre-distortion algorithm for RF power amplifier can also be
solved through establishing a neural network model orienting
to phase bias of RF power amplifier to improve efficiency of
RF power amplifier. When a neural network model orienting to
phase bias of RF power amplifier is established (the structure

of such a neural network is shown in Fig.6), the input signal and said delay items of the input signal are base-band digital signal amplitude $X(KT)$ of the power amplifier and delay items thereof $X[(K-1)T]$, $X[(K-2)T]$, ..., $X[(K-n+1)T]$ as well as phase $\Phi_{in}(KT)$ of the base-band digital signal and delay items thereof $\Phi_{in}[(K-1)T]$, $\Phi_{in}[(K-2)T]$, ..., $\Phi_{in}[(K-n+1)T]$; the number of delay items of the input signal comprises the number n_1 of delay items of base-band digital signal amplitude and the number n_2 of delay items of base-band digital signal phase.

In establishing the neural network model orienting to phase bias of RF power amplifier, the number of delay items of base-band digital signal is: $1 < n_1 < 5$, the number of delay items of base-band digital signal phase is: $1 < n_2 < 10$, the number of neural elements on each layer of the neural network is: $1 < r < 10$, the number of layers of the neural network is: $1 < m < 10$; in this case, the value of K is: $K=1$, the value of M is: $M > \text{the maximum I/O phase bias (i.e., } > \max \Delta \Phi)$.

The principle of the calculation for establishing the neural network model orienting to phase bias of RF power amplifier is identical to that of the calculation for calculating the AM-AM neural network model orienting to I/O amplitude relation for RF power amplifier.

Compared with traditional AM-PM models, the advantage of the neural network modelling method orienting to I/O phase bias of RF power amplifier according to the present invention is obvious. Traditional AM-PM models only correlate input amplitude with phase bias simply, not taking account into the

effects of phase size of input signal, amplitude variation rate of input signal, and input phase variation rate to I/O phase bias. Whereas the neural network model orienting to phase bias takes account into possible effects of all external factors to base-band phase bias of RF power amplifier through introducing input amplitude and the delay items thereof as well as input phase and the delay items thereof of RF power amplifier at the input end.

In the I/O modelling data of RF power amplifier used in the modelling process for establishing above two mathematical models for RF power amplifier, the bandwidth of input must be wider than that of actual inputs of RF power amplifier corresponding to above models, to ensure adequate accuracy of the mathematical models for the RF power amplifier. That is to say, suppose the operating bandwidth of CDMA or WCDMA system for the RF power amplifier is kF (wherein k is an integer, F is bandwidth of individual carrier), the design principle for bandwidth F_m of modelling signals is:

$$F_m > kF.$$

Wherein, a bandwidth of at least an additional out-of-band carrier is required. In addition, the amplitude and/or phase bias of modelling signals should be high enough, in order to cover actual operation range of RF power amplifier with sufficient data.

The AM-AM neural network modelling method for RF power amplifier has been verified with experimental data (5MHz bandwidth modelling signals). As shown in Fig.8, the AM-AM neural network model is trained (i.e., modelling) and then verified with another 1.25MHz bandwidth signal, as shown in

Fig.9; the result is shown in Fig.10, in which the difference between model data and actual data of RF power amplifier is almost imperceptible. In addition, it is easy to solve the corresponding pre-distortion algorithm with that model and the solving difficulty will not aggravate as model size expands, in contrast to the case of polynomial modelling method; after the pre-distortion algorithm is solved, the object of improving efficiency of RF power amplifier can be attained with the pre-distortion algorithm.